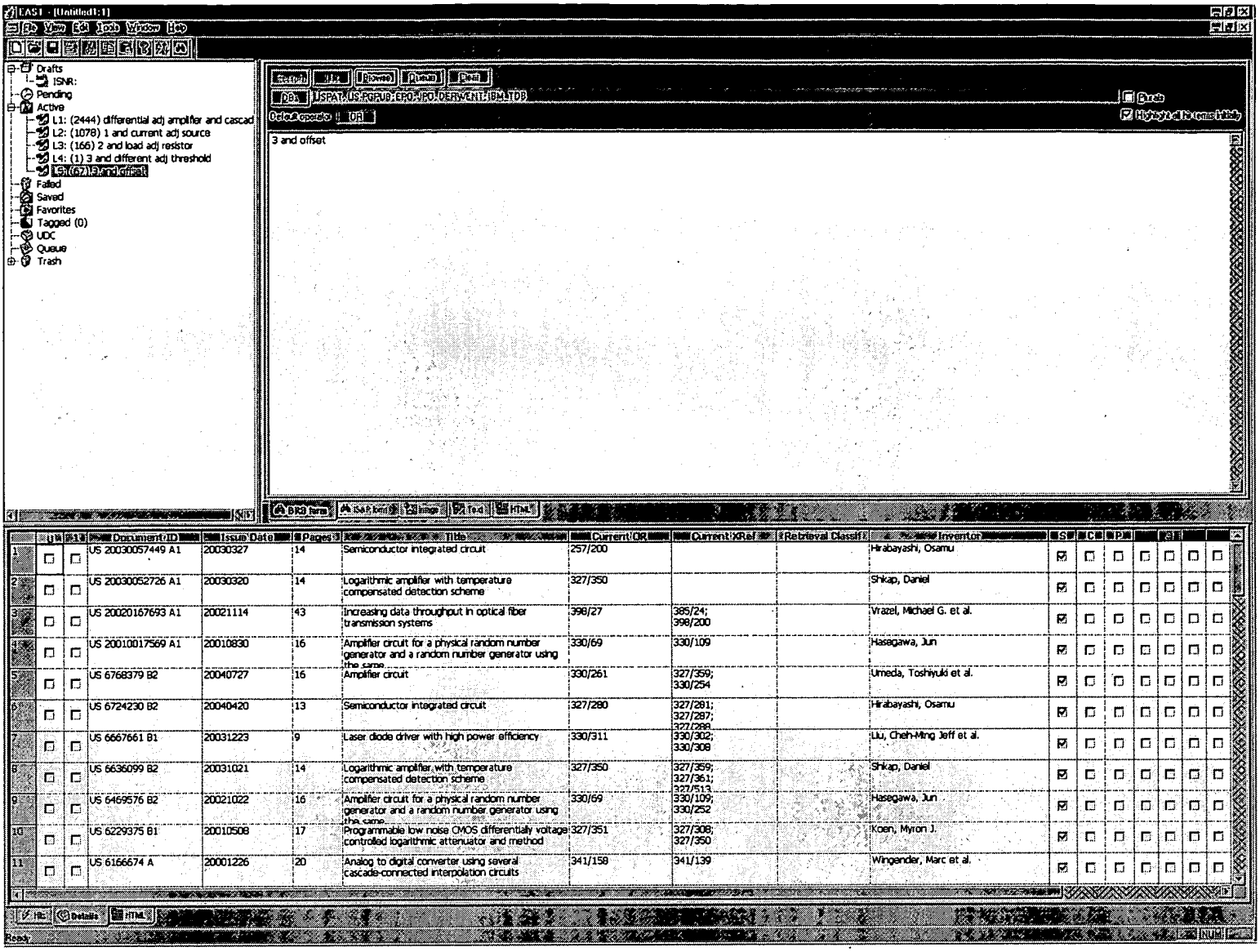
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Drafts
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US 20030057449 A1 20030327 14 Semiconductor integrated circuit 257/200 Hirabayashi, Osamu
US 20030052726 A1 20030320 14 Logarithmic amplifier with temperature compensated detection scheme 327/350 Shkap, Daniel
US 20020167693 A1 20021114 43 Increasing data throughput in optical fiber transmission systems 398/27 385/24; 398/200 Vrazel, Michael G. et al.
US 20010017569 A1 20010830 16 Amplifier circuit for a physical random number generator and a random number generator using the same 330/69 330/109 Hasegawa, Jun
US 6768379 B2 20040727 16 Amplifier circuit 330/261 327/359; 330/254 Umeda, Toshiyuki et al.
US 6724230 B2 20040420 13 Semiconductor integrated circuit 327/280 327/281; 327/287; 327/288 Hirabayashi, Osamu
US 6667661 B1 20031223 9 Laser diode driver with high power efficiency 330/311 330/302; 330/308 Liu, Cheh-Ming Jeff et al.
US 6636099 B2 20031021 14 Logarithmic amplifier with temperature compensated detection scheme 327/350 327/359; 327/361; 327/513 Shkap, Daniel
US 6469576 B2 20021022 16 Amplifier circuit for a physical random number generator and a random number generator using the same 330/69 330/109; 330/252 Hasegawa, Jun
US 6229375 B1 20010508 17 Programmable low noise CMOS differentially voltage controlled logarithmic attenuator and method 327/351 327/308; 327/350 Koen, Myron J.
US 6166674 A 20001226 20 Analog to digital converter using several cascade-connected interpolation circuits 341/158 341/139 Wingender, Marc et al.
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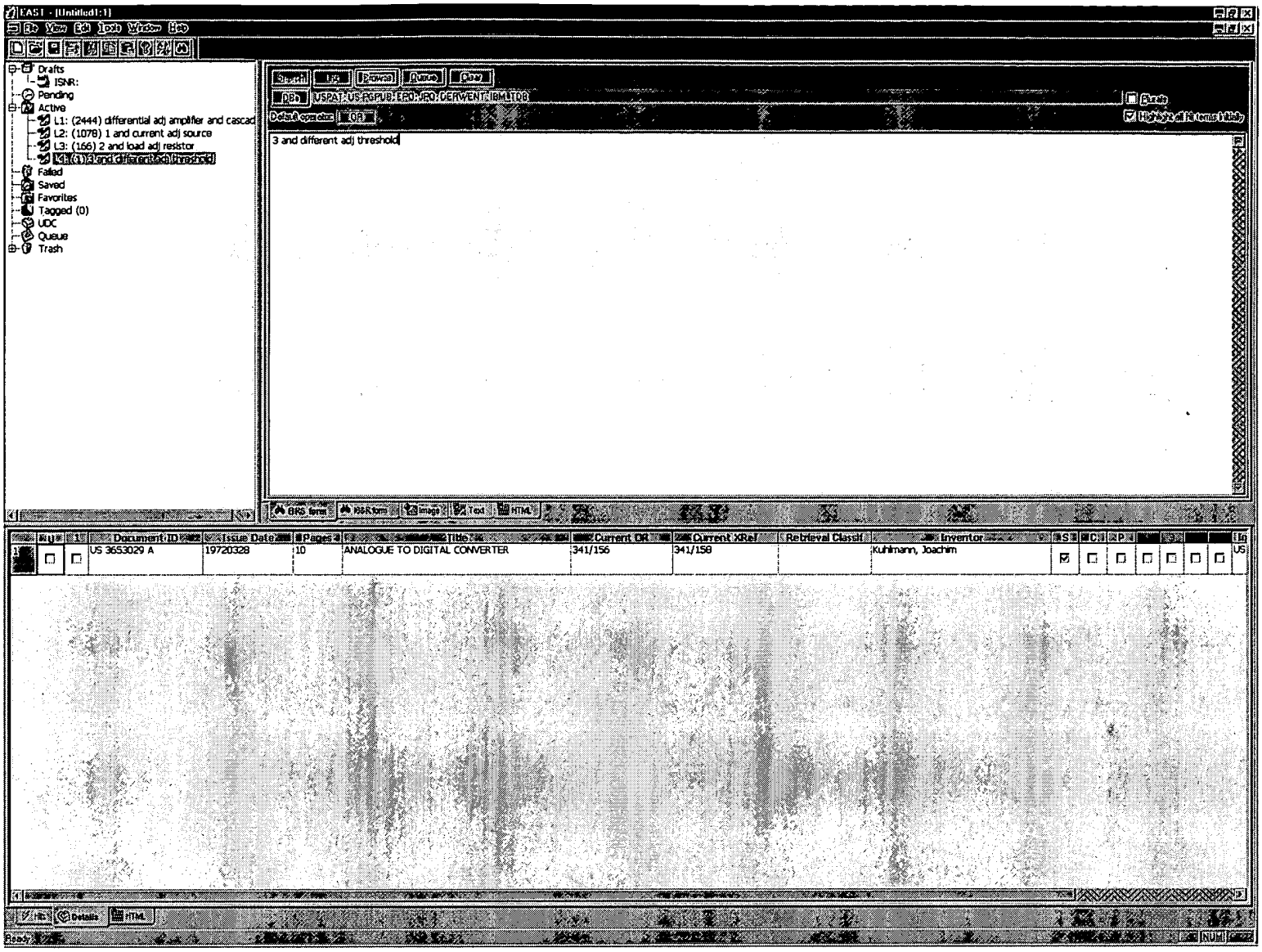
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ANALOGUE TO DIGITAL CONVERTER
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